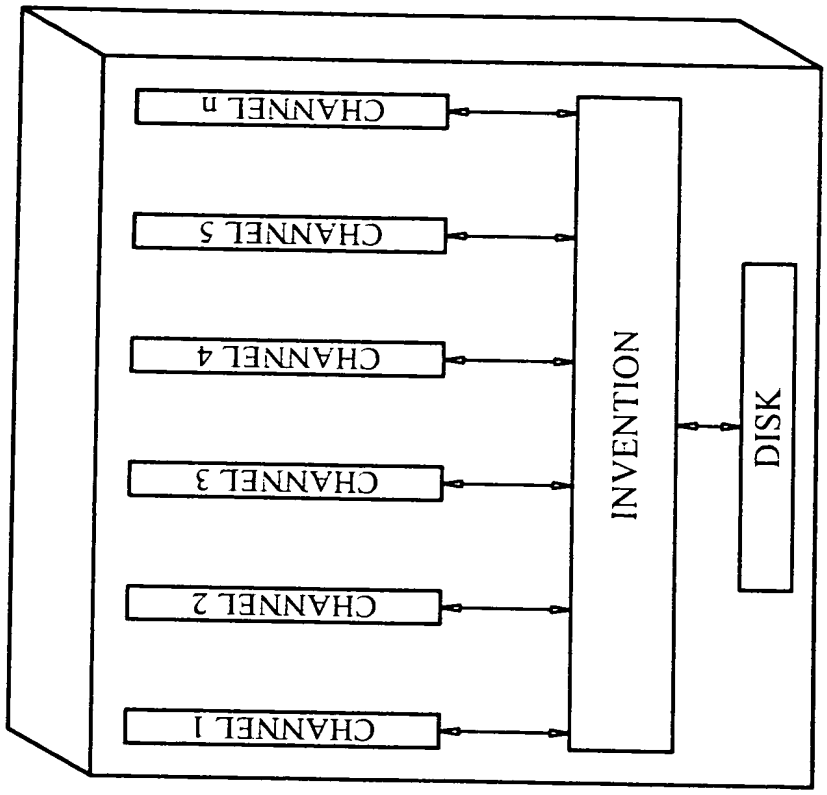
CHANNEL n
CHANNEL 5
CHANNEL 4
CHANNEL 3
CHANNEL 2
CHANNEL 1
INVENTION
DISK

FIG. 2

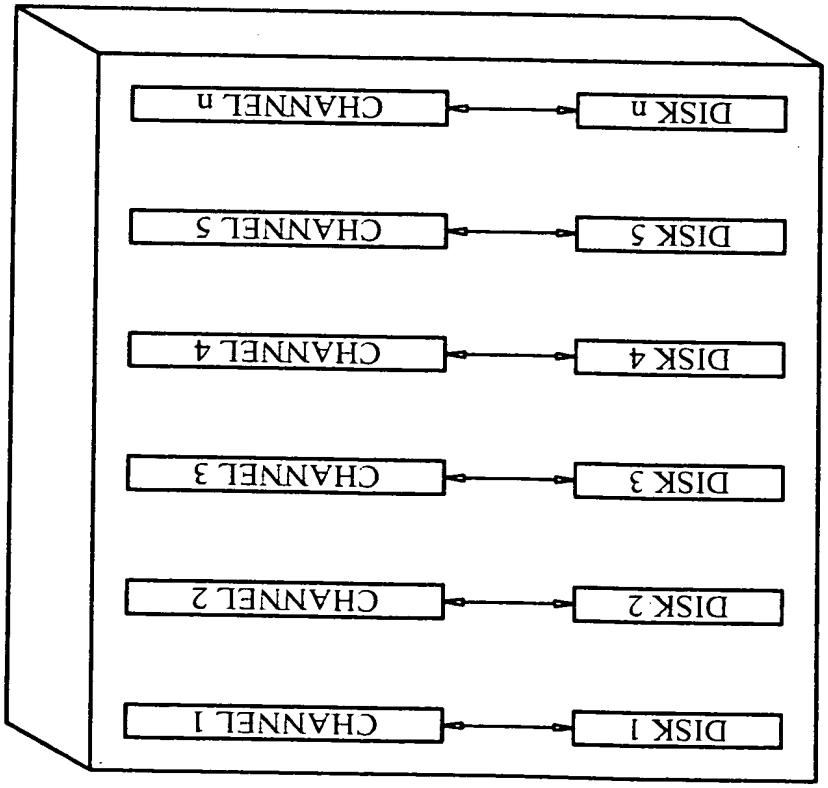
CHANNEL n
DISK n
CHANNEL 5
DISK 5
CHANNEL 4
DISK 4
CHANNEL 3
DISK 3
CHANNEL 2
DISK 2
CHANNEL 1
DISK 1

FIG. 1

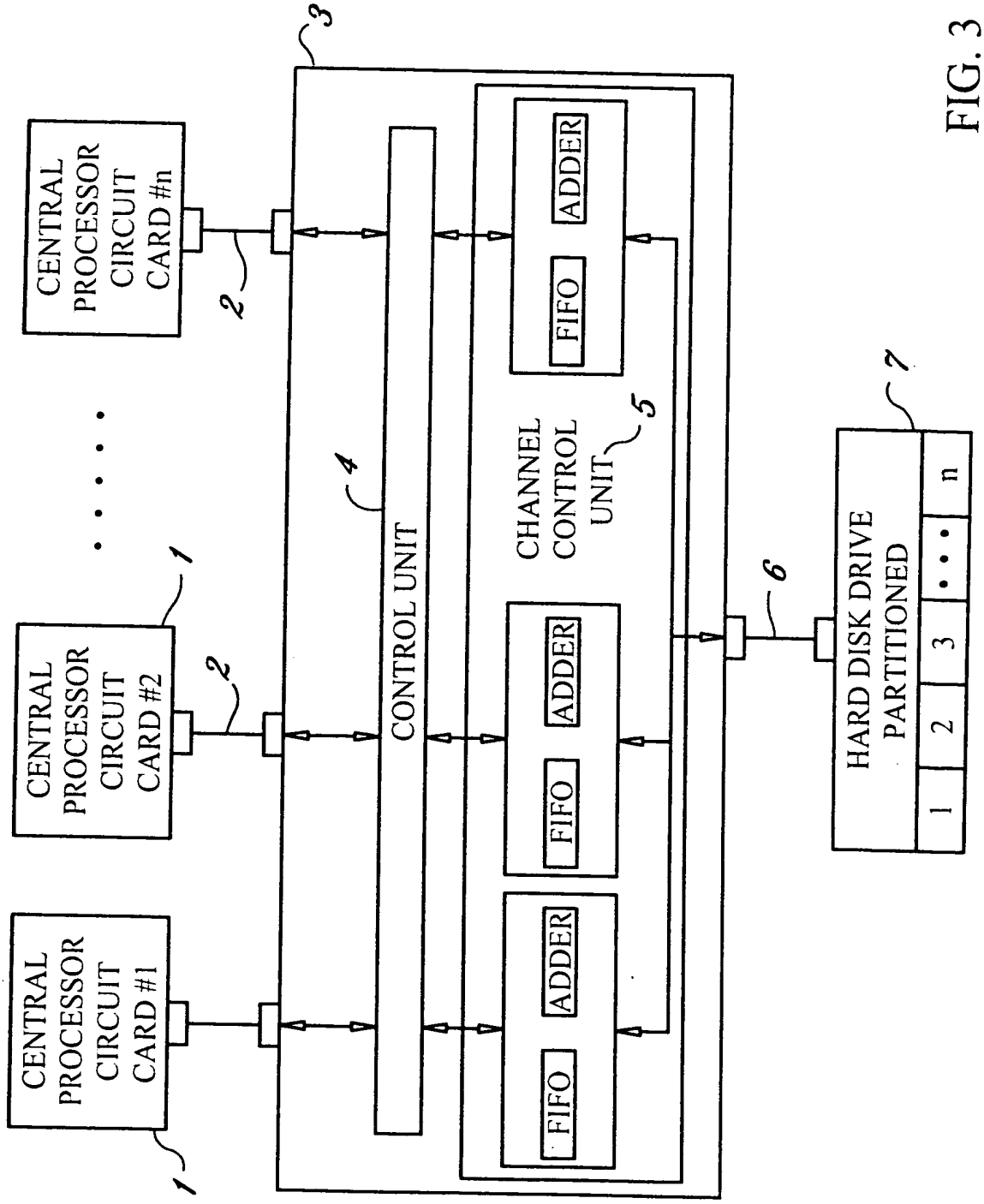
CENTRAL PROCESSOR CIRCUIT CARD #1
CENTRAL PROCESSOR CIRCUIT CARD #2
CENTRAL PROCESSOR CIRCUIT CARD #n
CONTROL UNIT
CHANNEL CONTROL UNIT
FIFO
ADDER
HARD DISK DRIVE PARTITIONED
1
2
3
n
1
2
3
4
5
6
7

FIG. 3

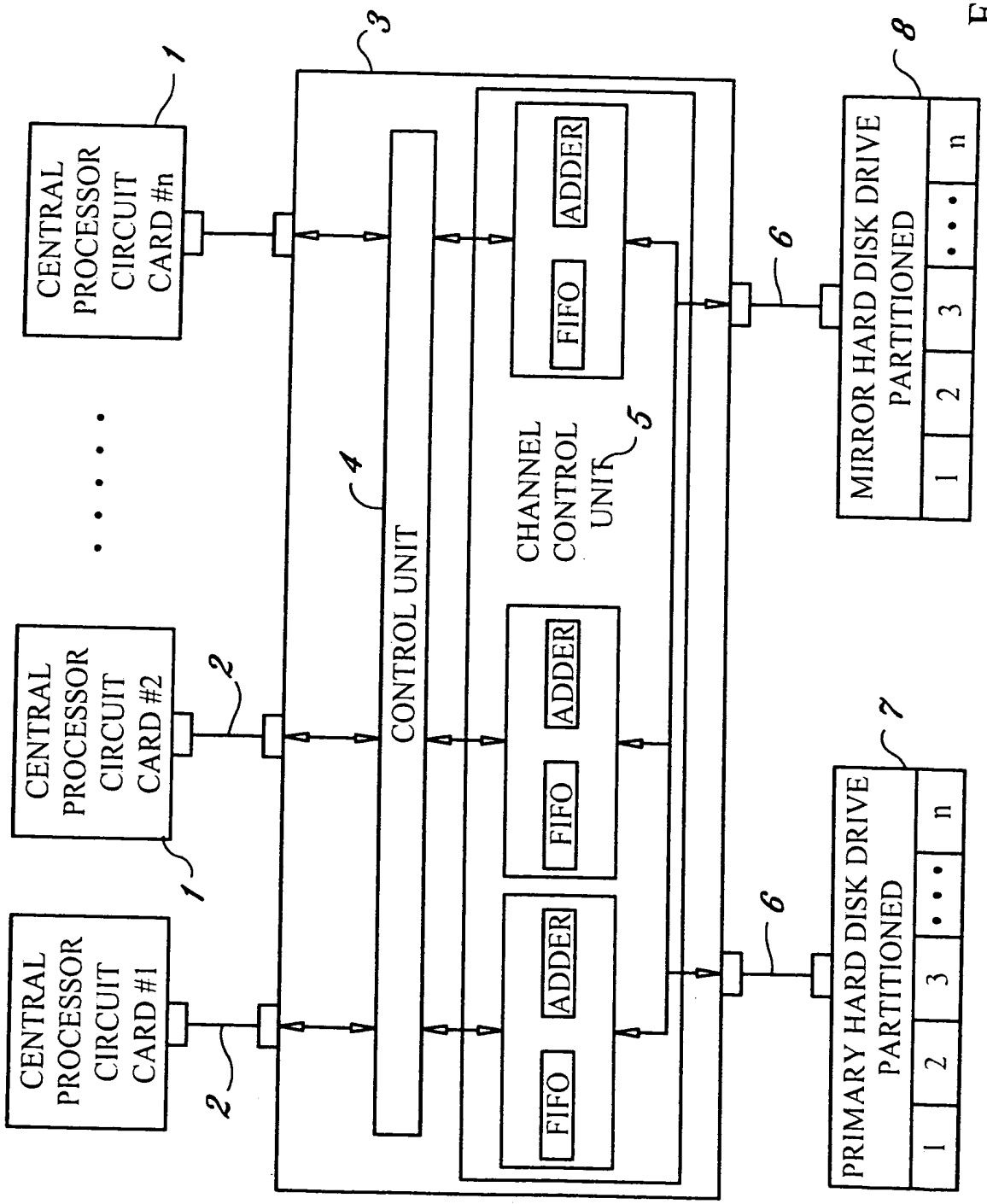
CENTRAL PROCESSOR CIRCUIT CARD #n
CENTRAL PROCESSOR CIRCUIT CARD #2
CENTRAL PROCESSOR CIRCUIT CARD #1
CONTROL UNIT
CHANNEL CONTROL UNIT
FIFO
ADDER
MIRROR HARD DISK DRIVE PARTITIONED
PRIMARY HARD DISK DRIVE PARTITIONED
1
2
3
n
1
2
3
4
5
6
7
8

FIG. 4

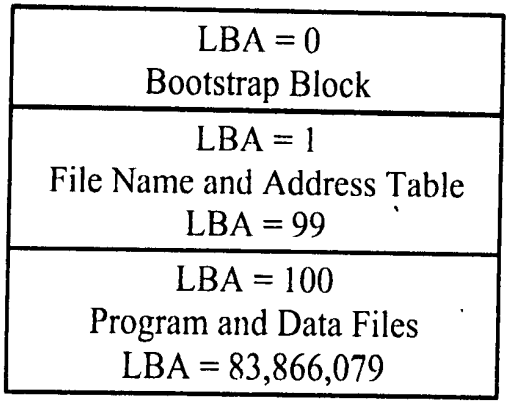

FIG. 5

PARTITION	STARTING LBA ON HARD DISK	ENDING LBA ON HARD DISK	STARTING LBA WITH INVENTION	ENDING LBA WITH INVENTION	DISK USAGE
1	0	83,866,079	0	83,866,079	40 GB
2	83,866,080	251,658,239	0	167,772,159	80 GB
3	251,658,240	335,544,319	0	83,866,079	40 GB
4	335,544,320	419,430,399	0	83,866,079	40 GB

FIG. 6

DESIRED SYSTEM BLOCK	PARTITION (COMPUTER)	OFFSET LBA ON (PARTITION-1)	BACKUP BLOCK ACCESSED
1,000,000	1	0	1,000,000
1,001,000	2	83,866,080	84,867,080
1,050,000	3	251,658,240	252,708,240
123	4	335,544,320	335,544,443

FIG. 7

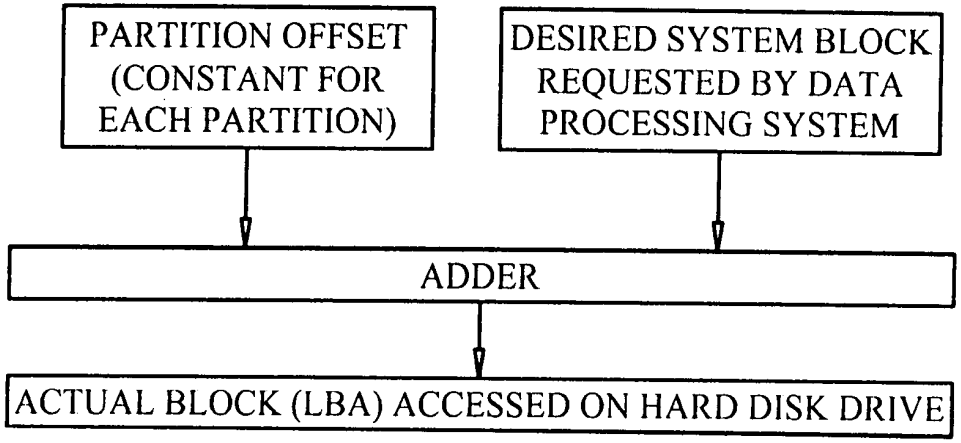

FIG. 8